

FIG. 1 (CR-ECS configuration)

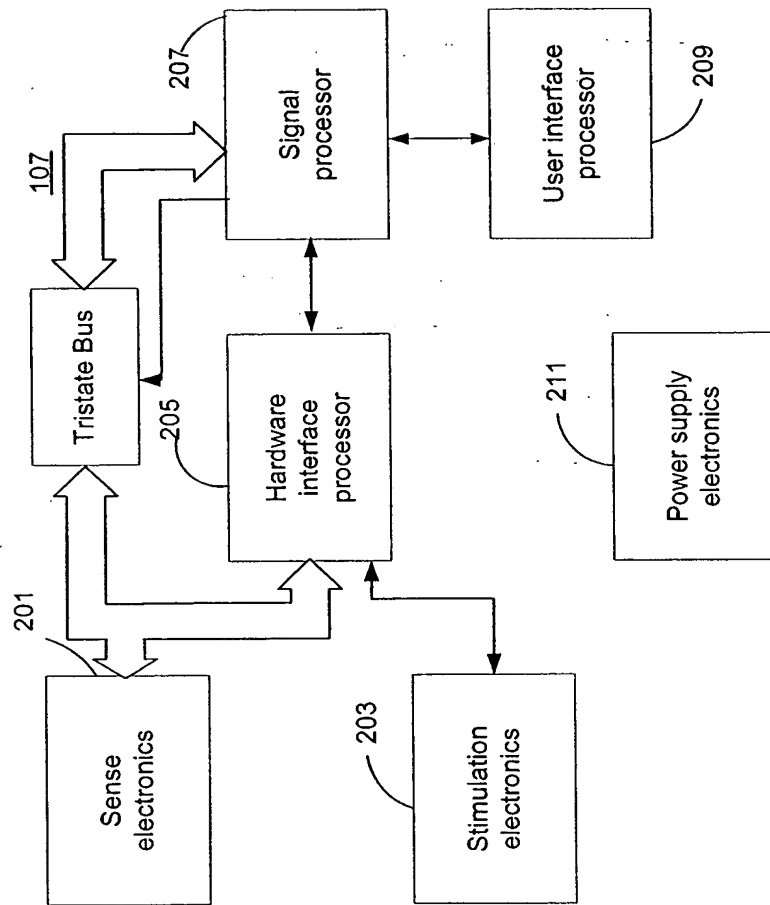


FIG. 2 (Bedside device configuration)

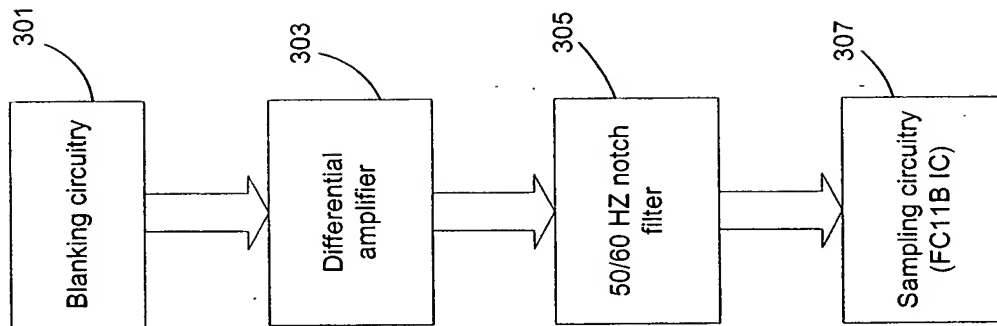


FIG. 3  
Sense electronics configuration

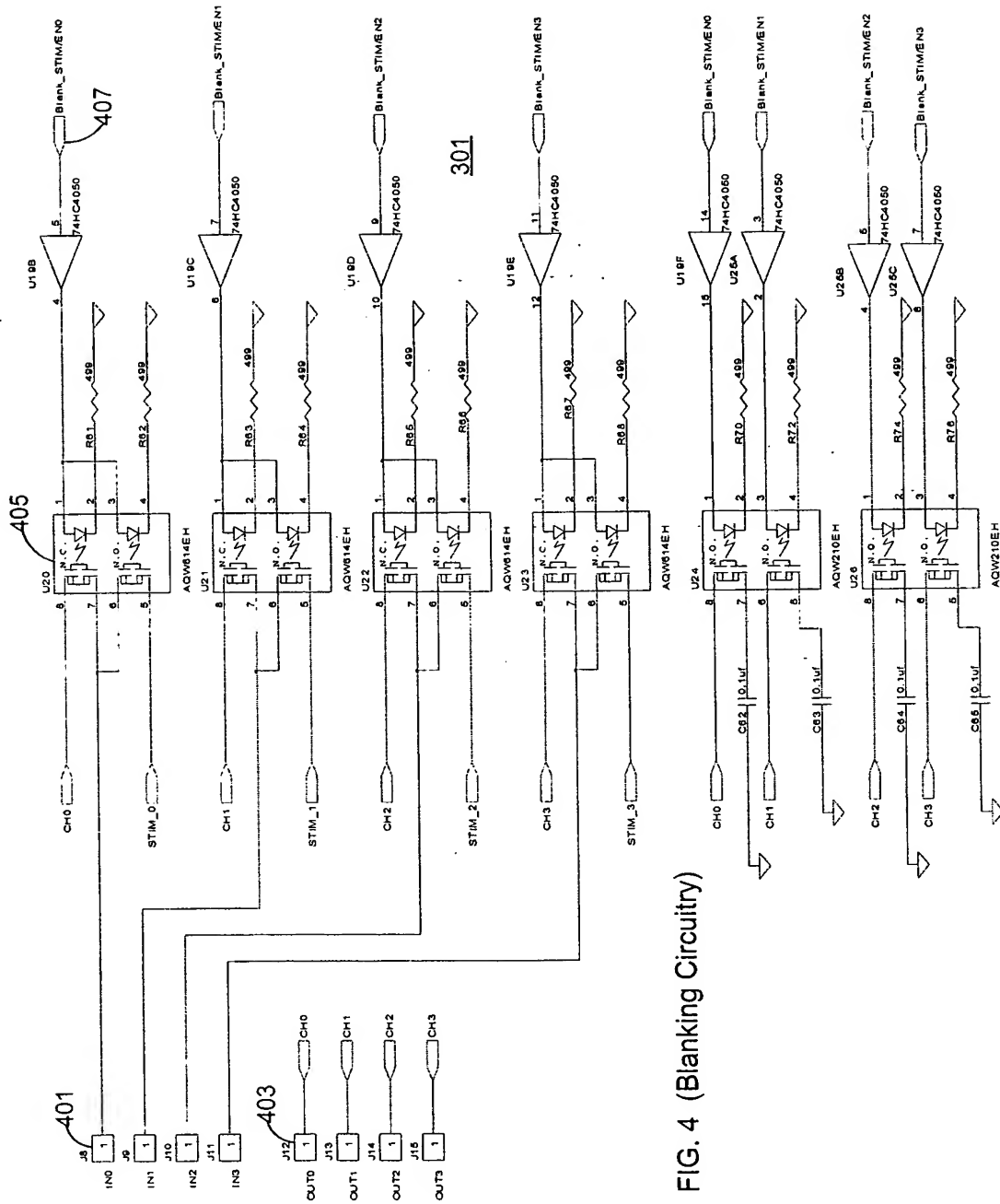


FIG. 4 (Blanking Circuitry)

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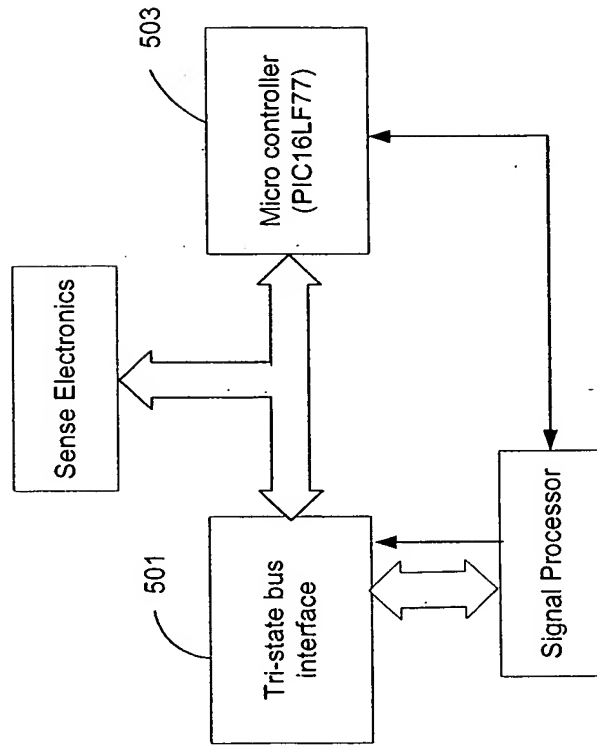


FIG. 5 (Hardware interface processor)

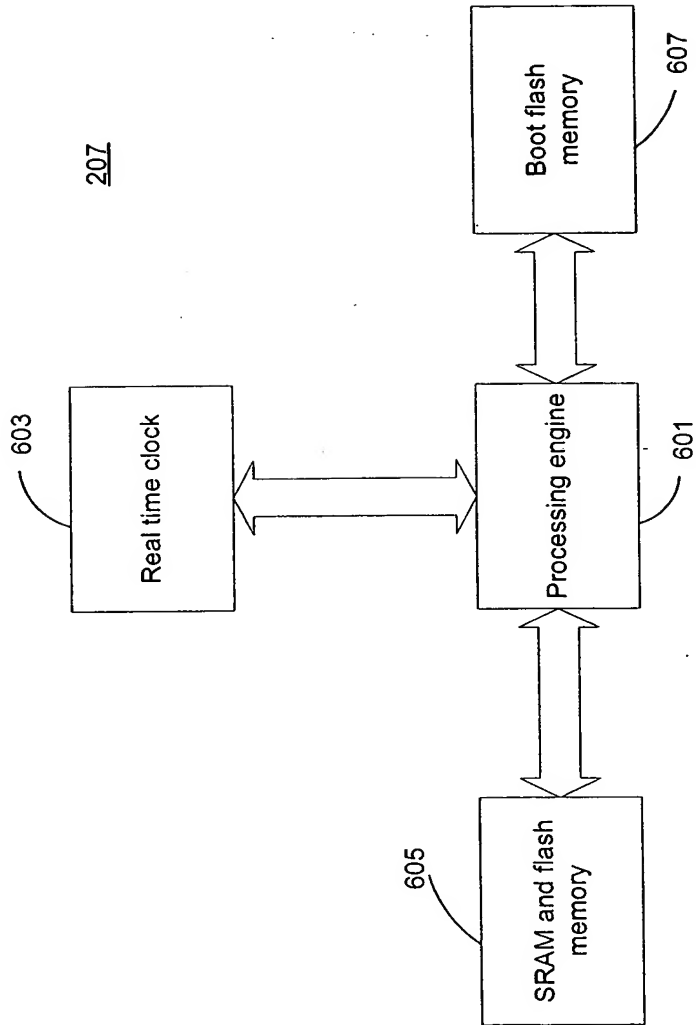


FIG. 6 (Signal processor)

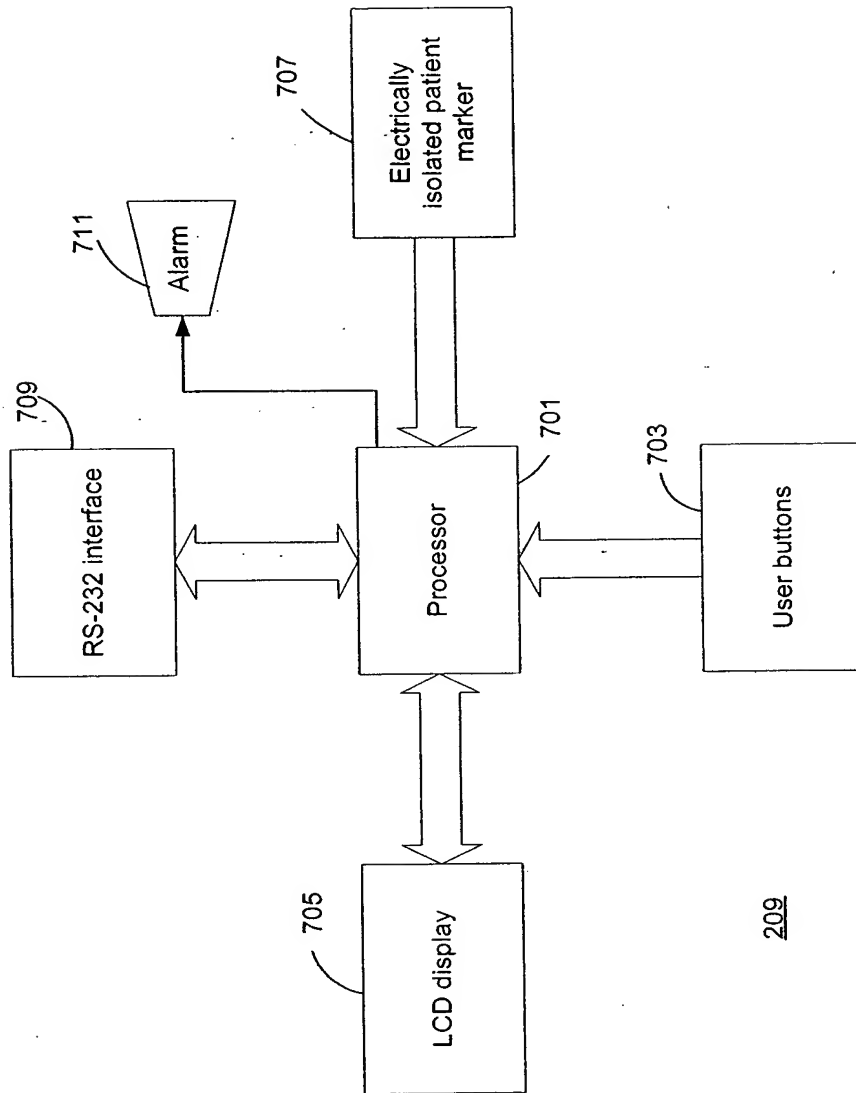


FIG. 7 (User interface processor)

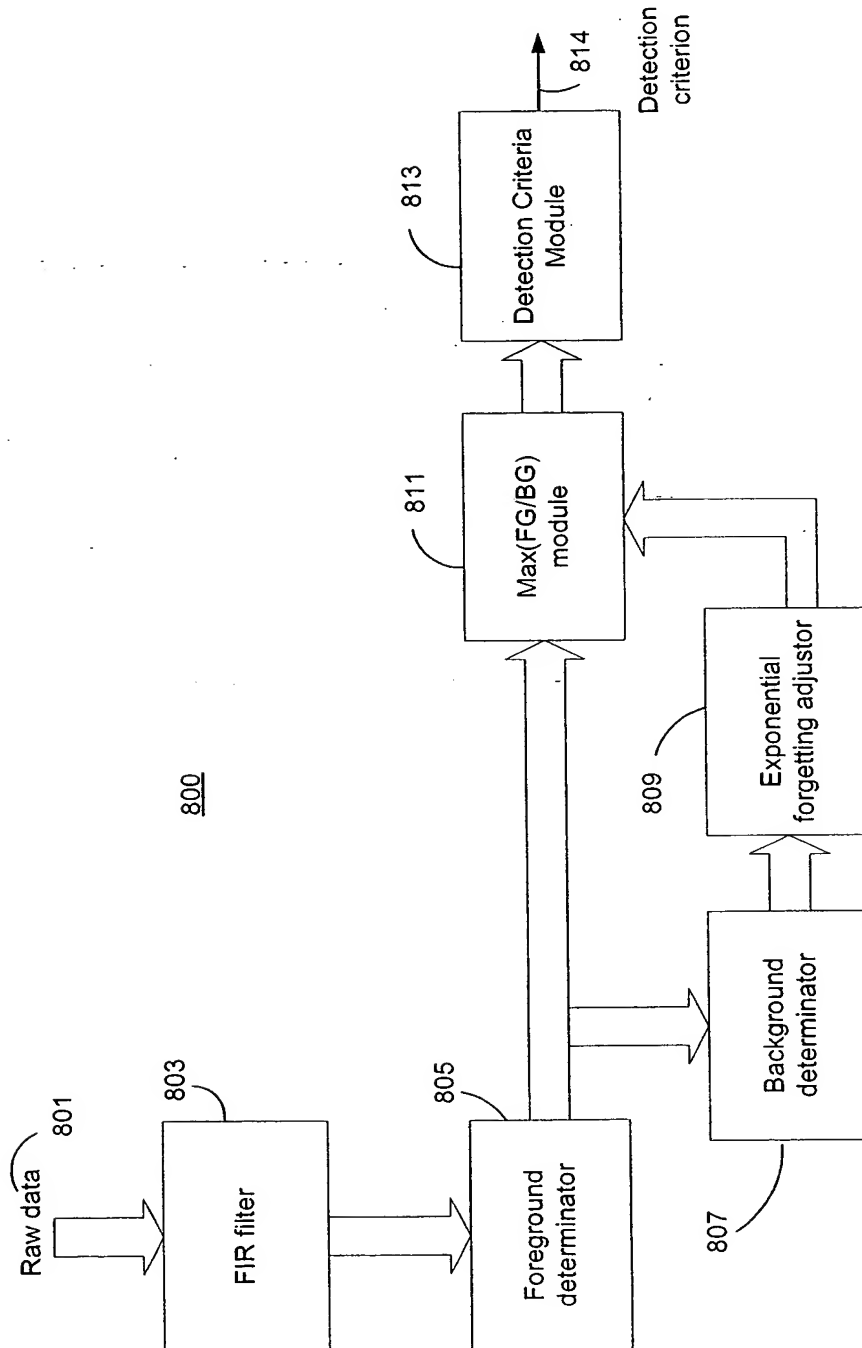


FIG. 8 (Seizure detection algorithm)



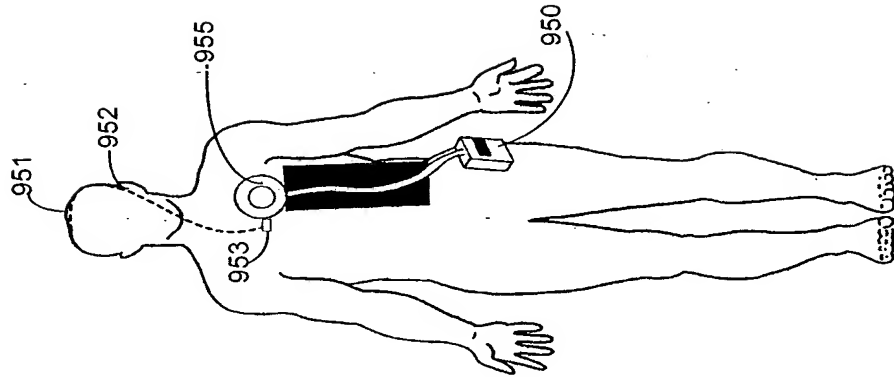


FIG. 9

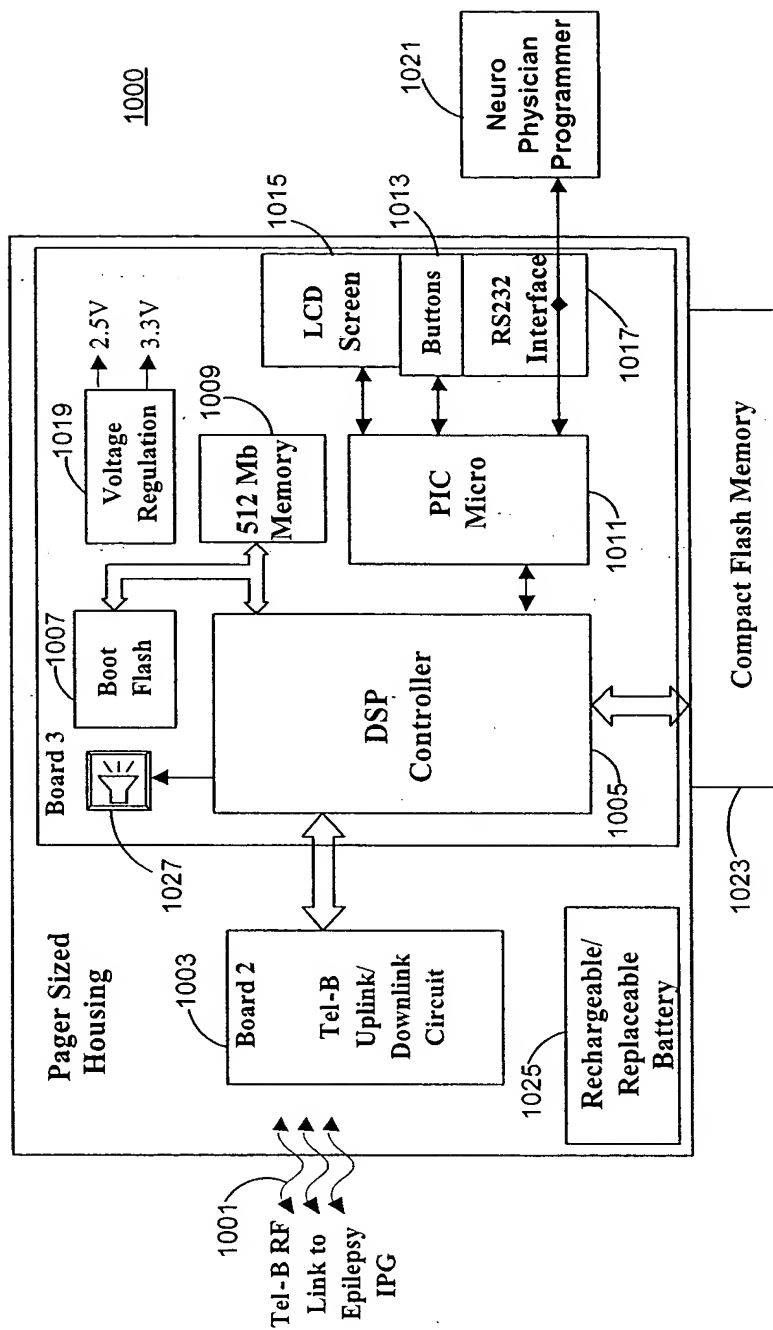


FIG. 10 (Epilepsy external device diagram)

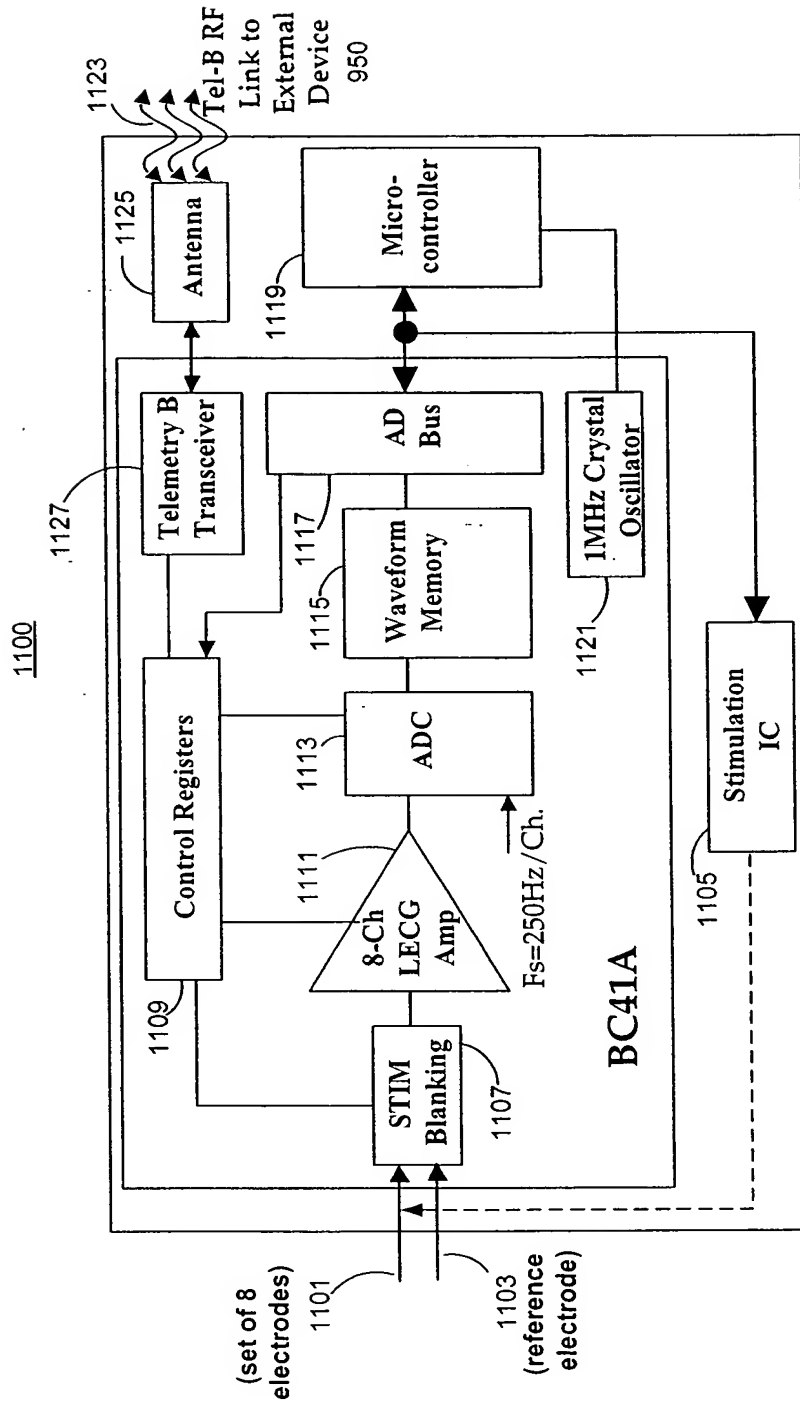


FIG. 11 (Epilepsy implantable device diagram)

FIG. 12

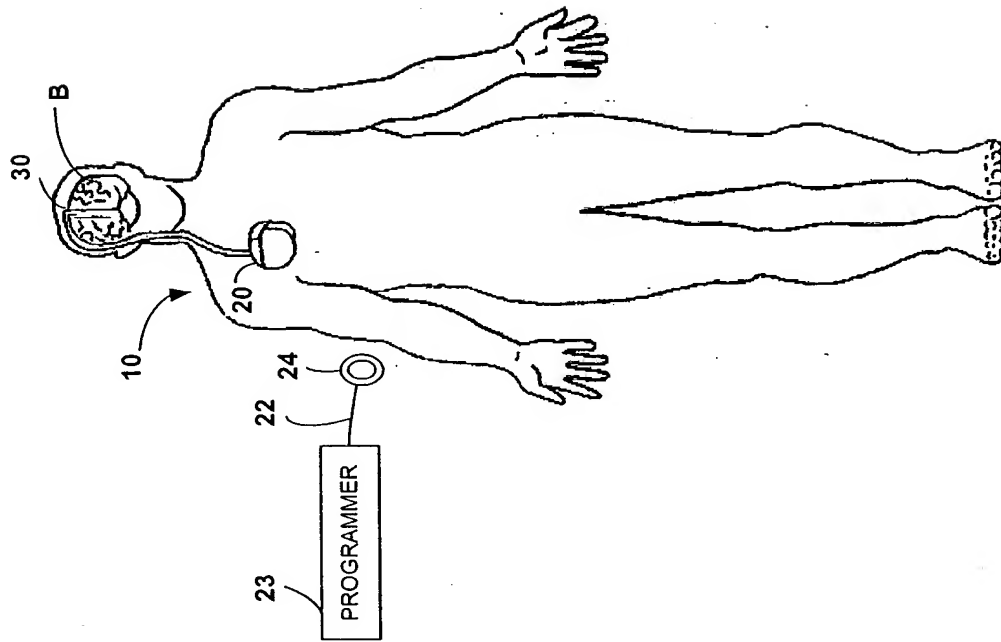
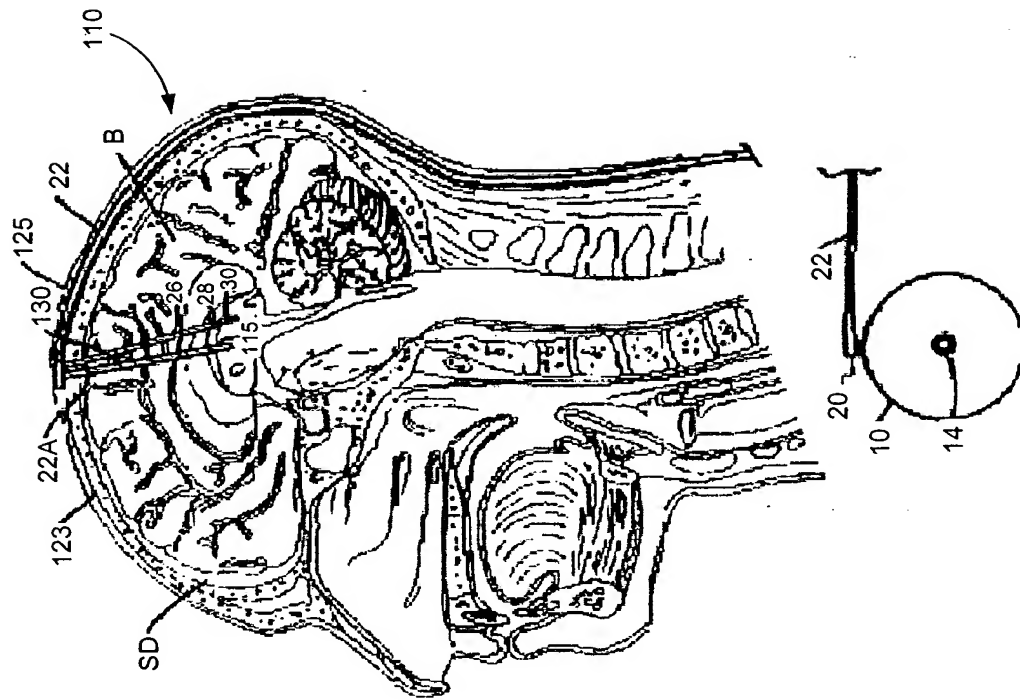


FIG. 13



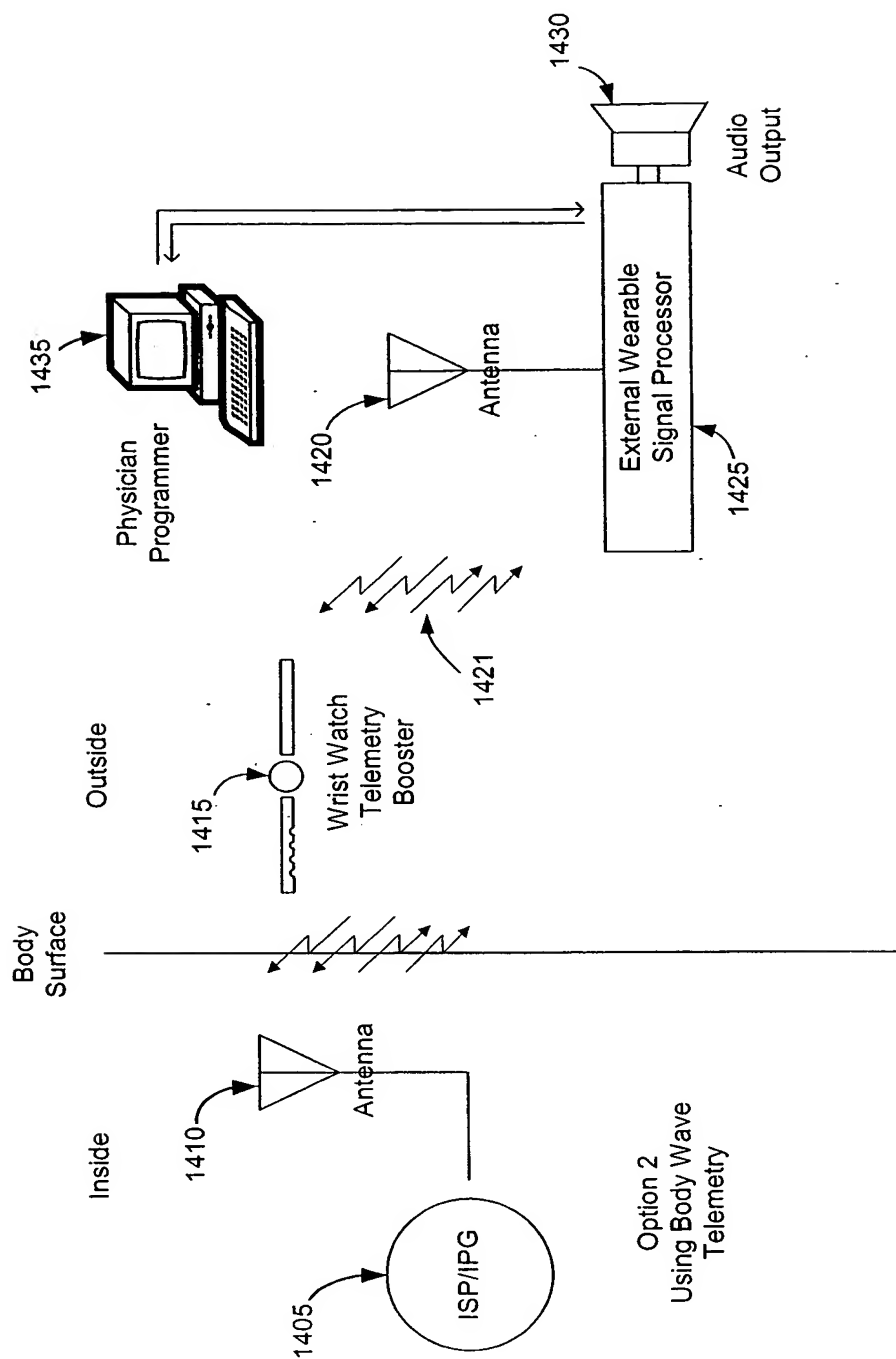
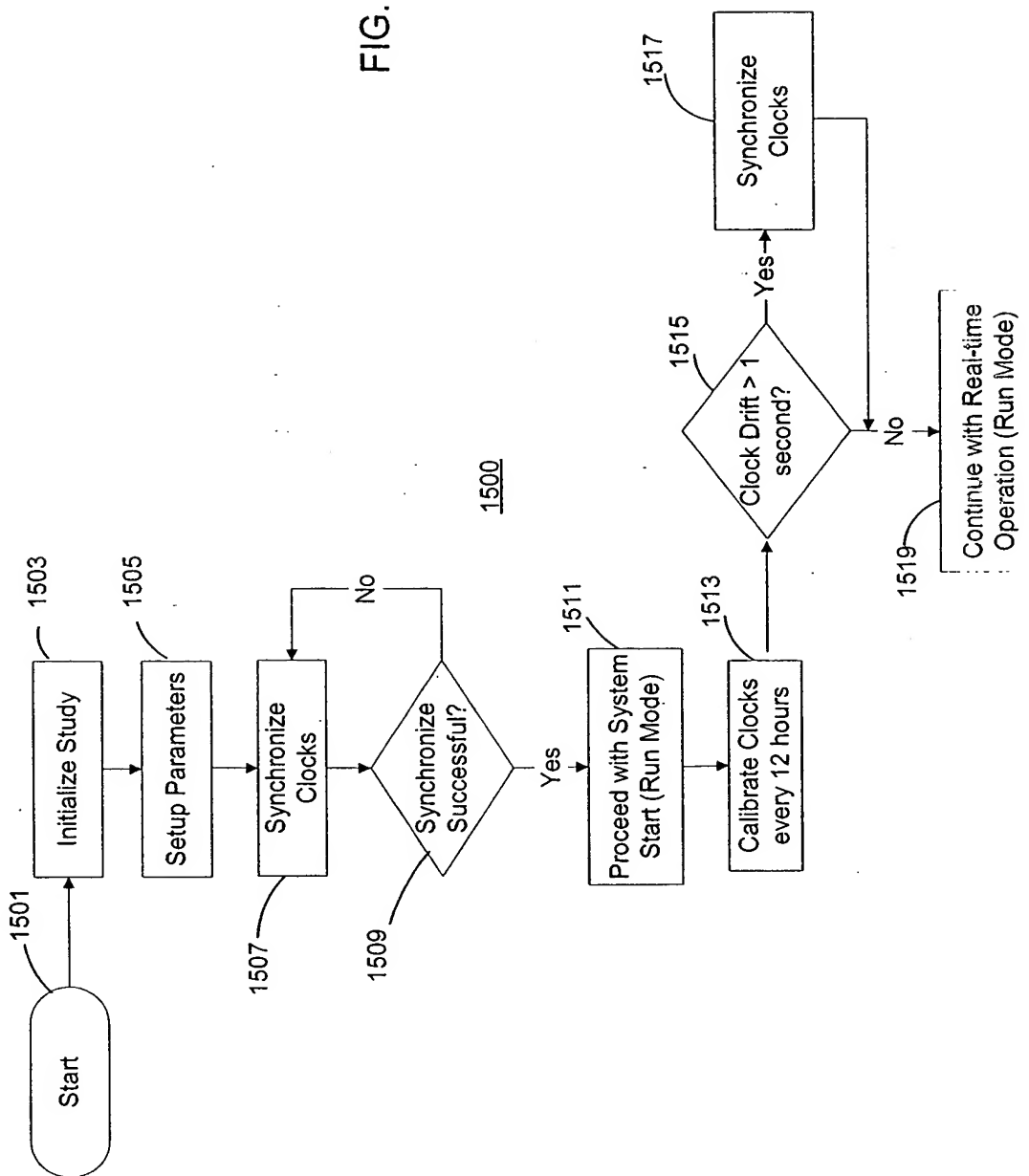


FIG. 14

FIG. 15



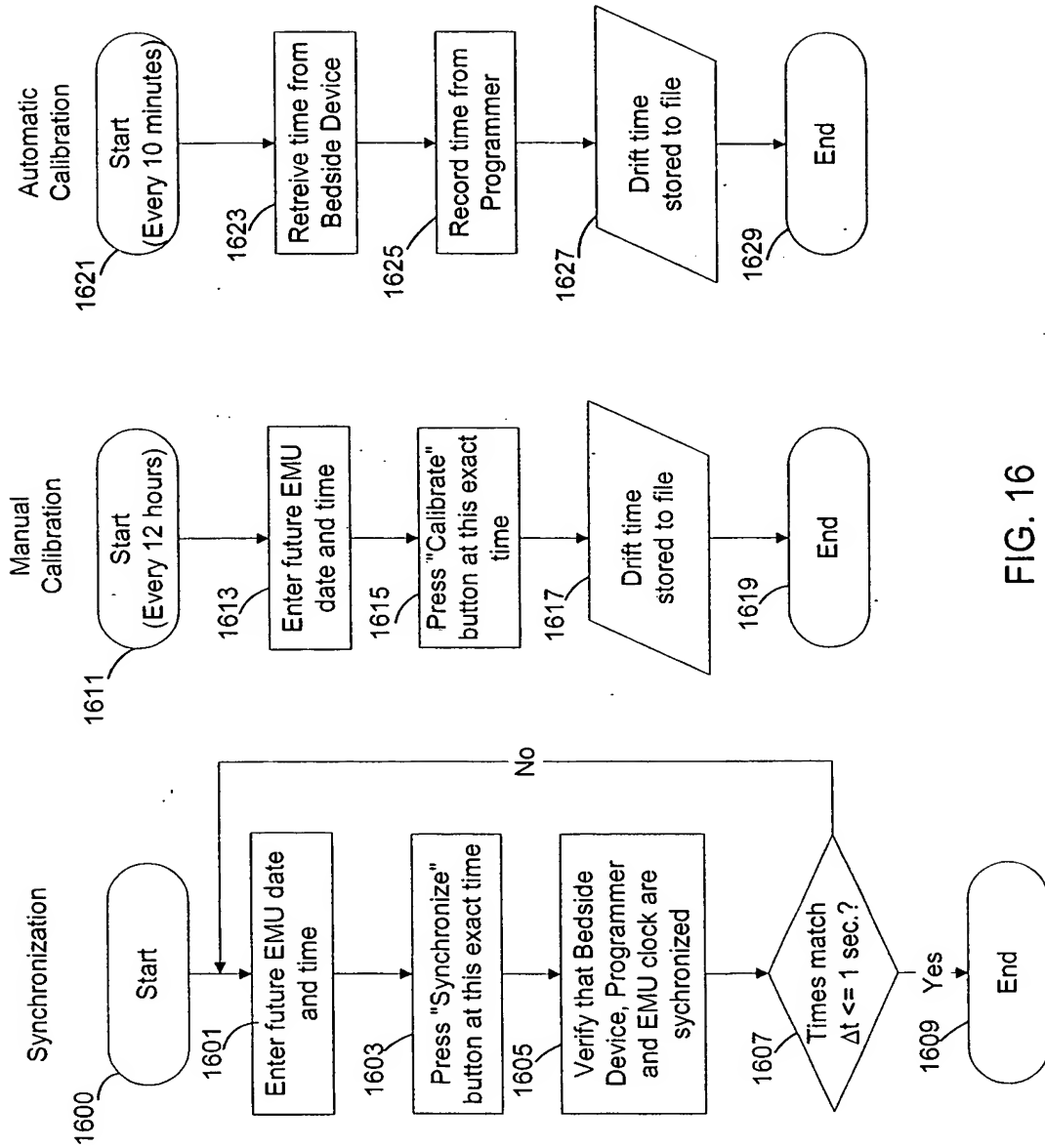


FIG. 16



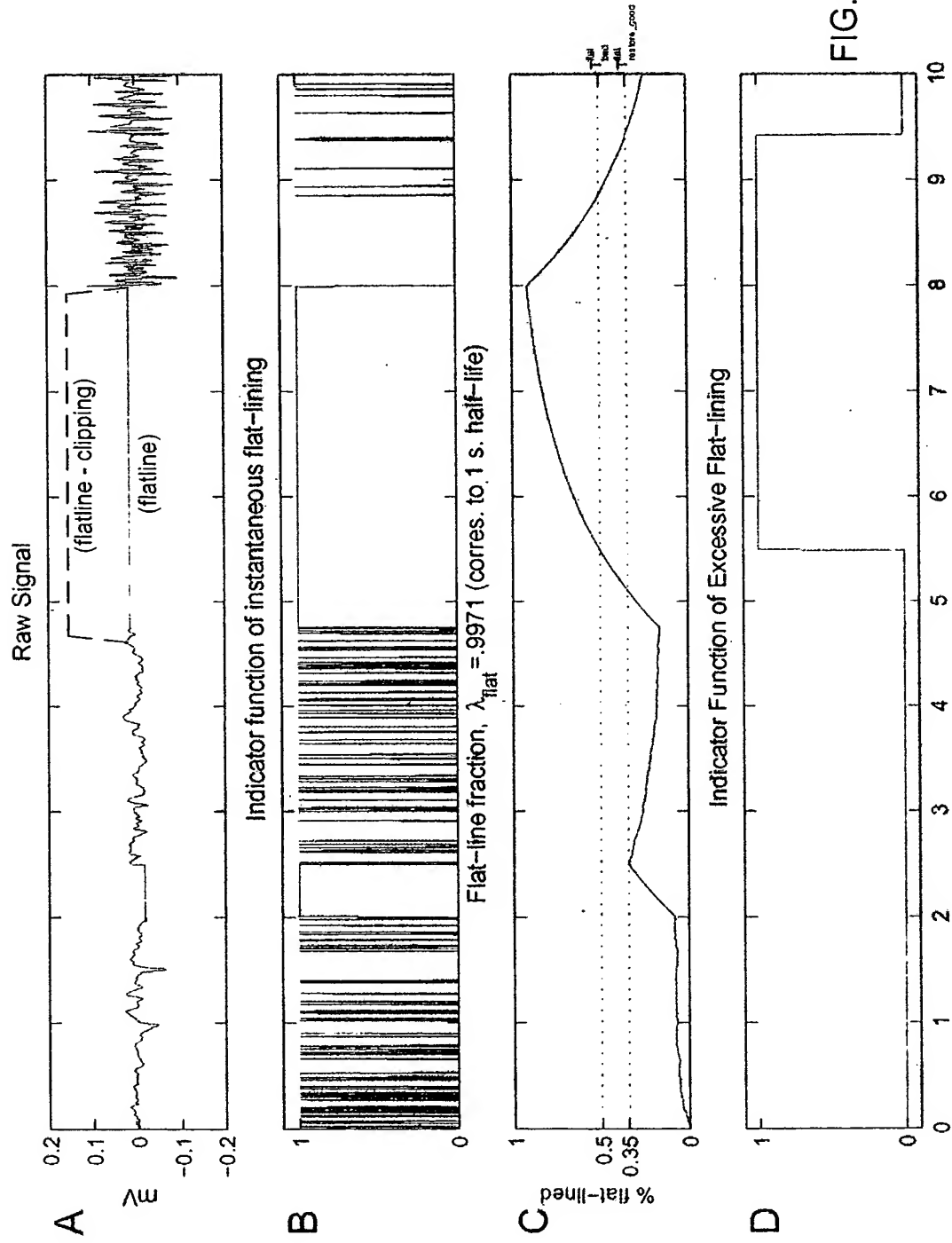


FIG. 17

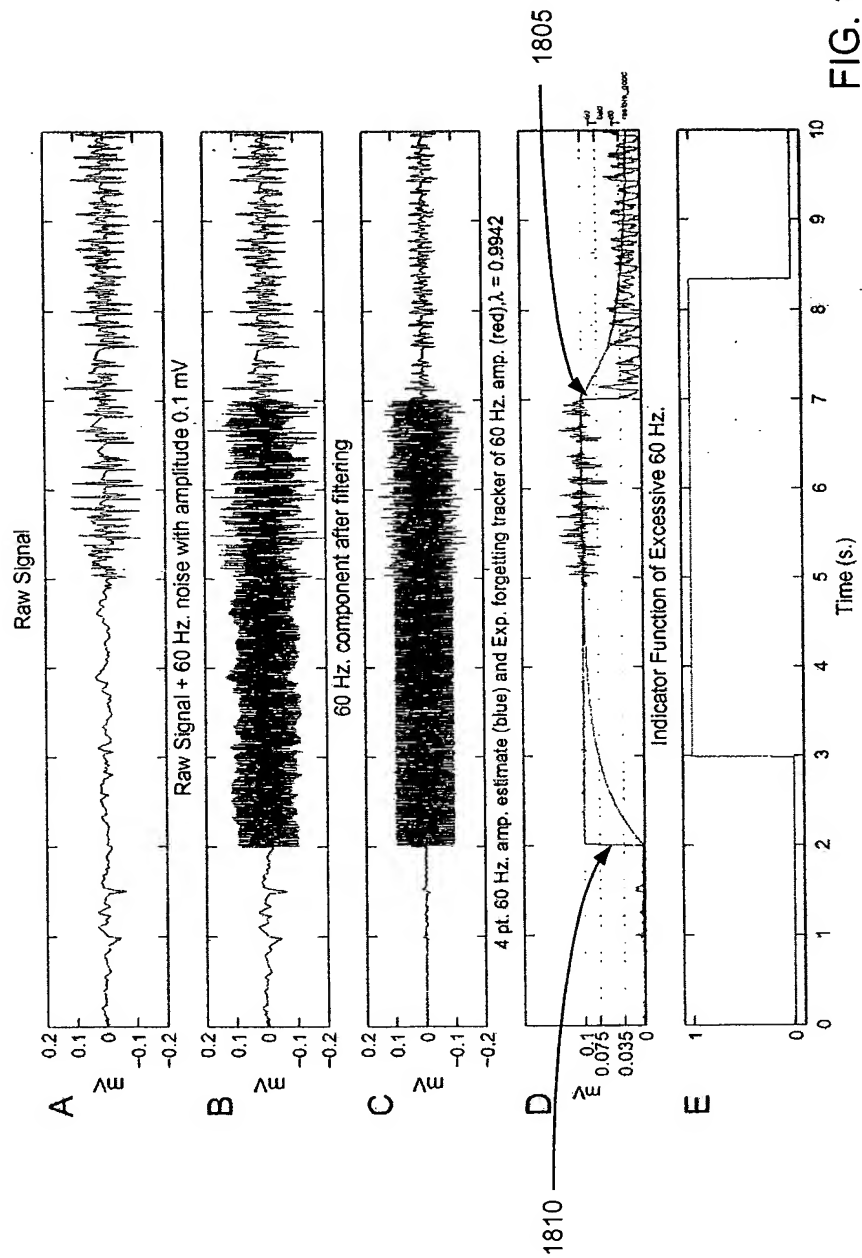


FIG. 18

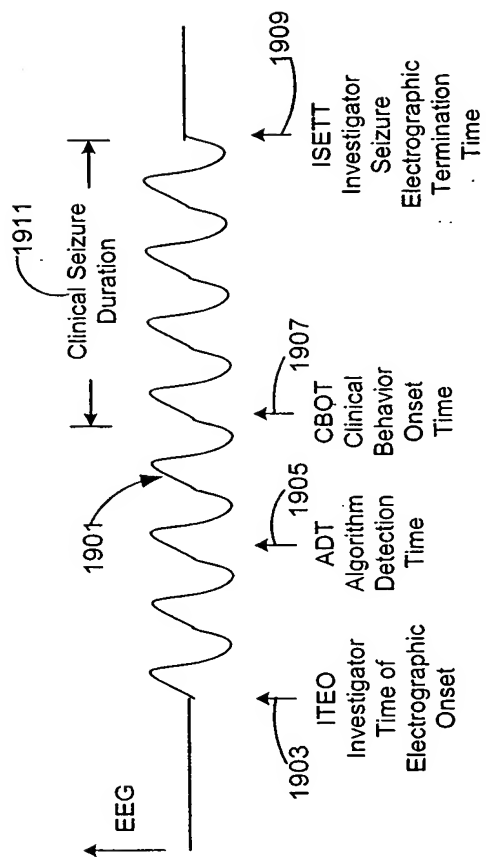


FIG. 19

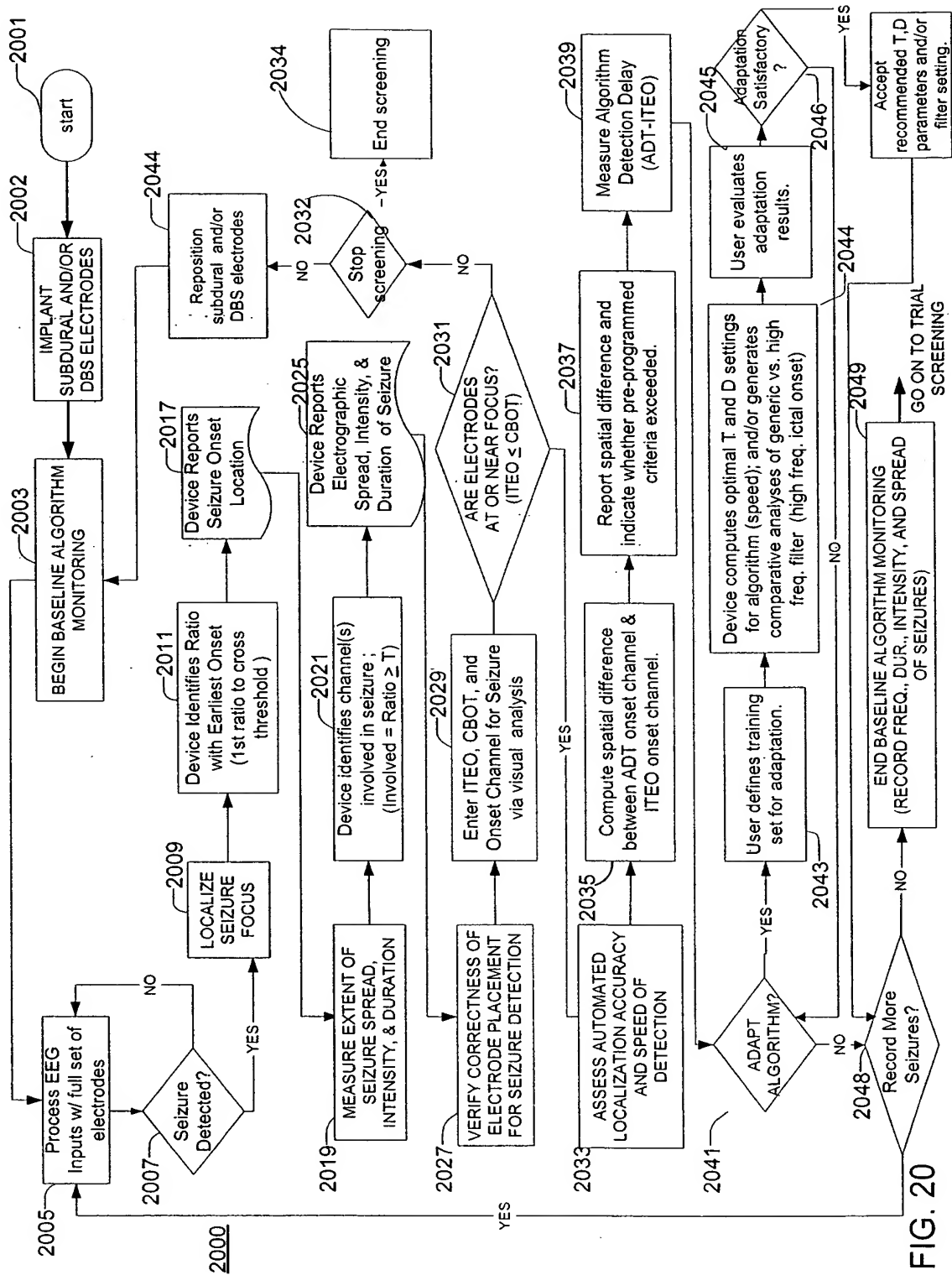


FIG. 20

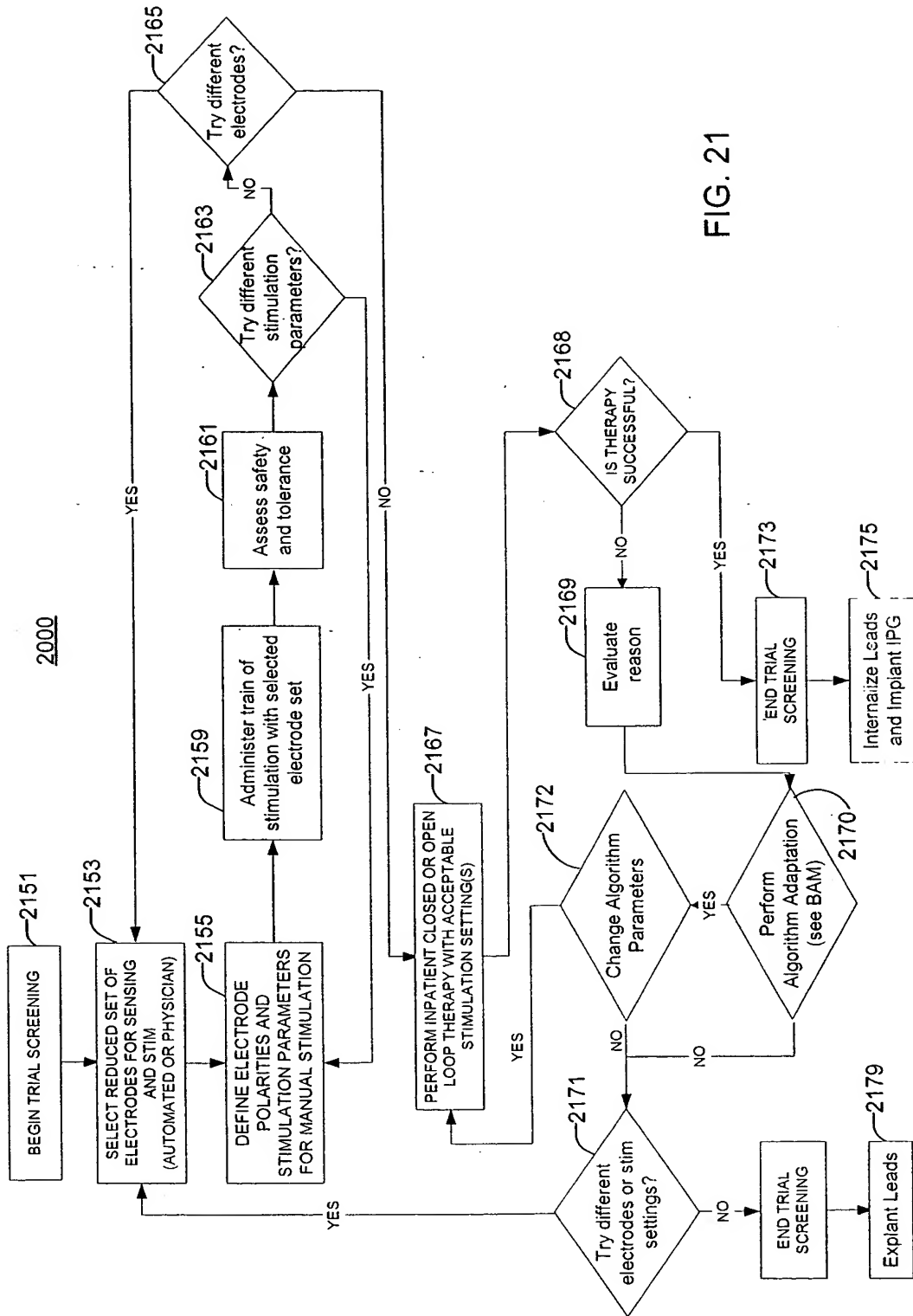


FIG. 21

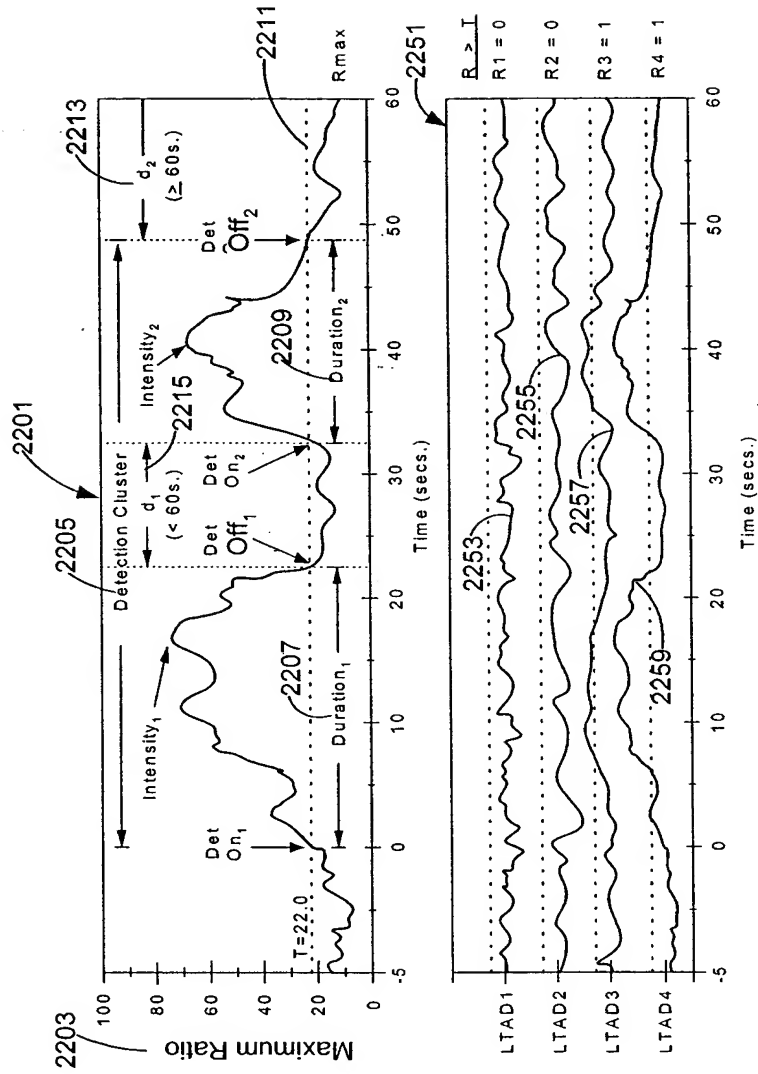


FIG. 22 (Clustering)

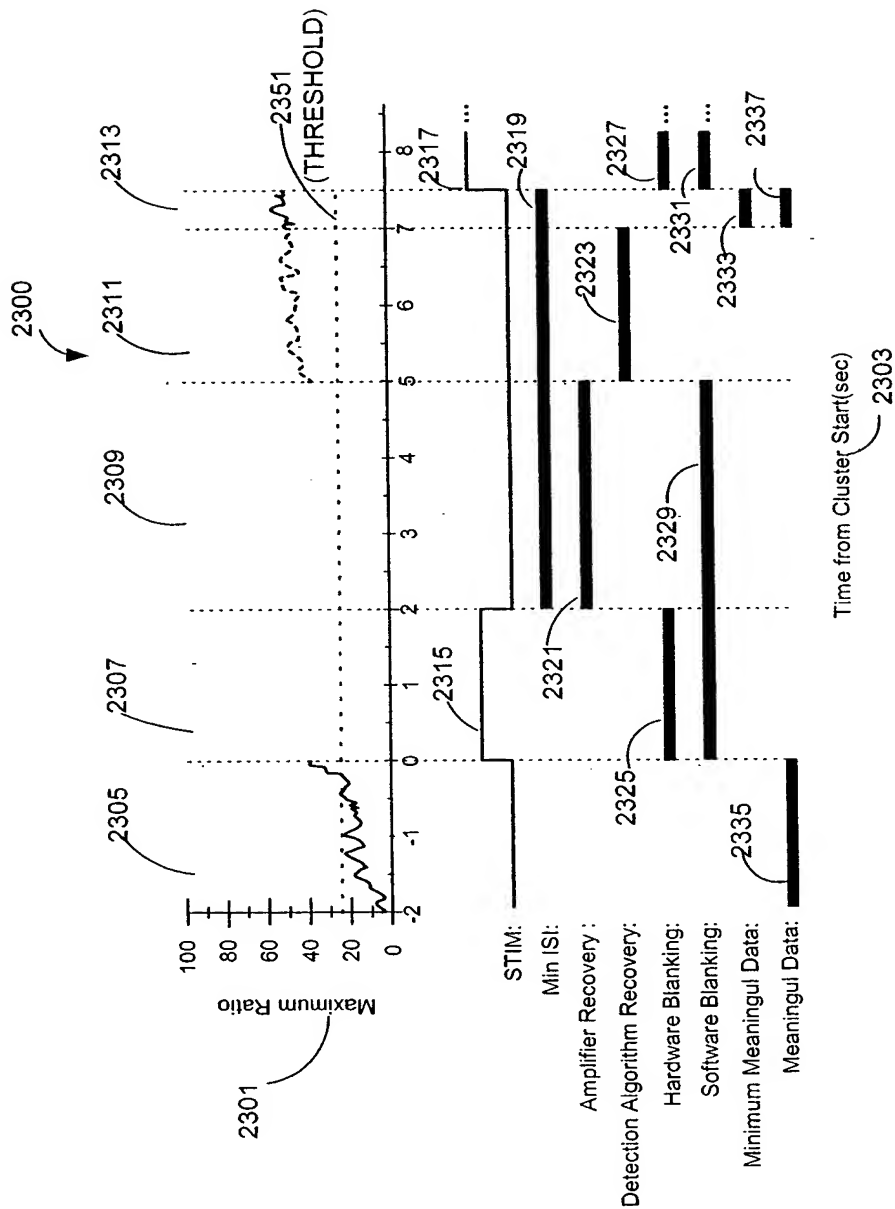
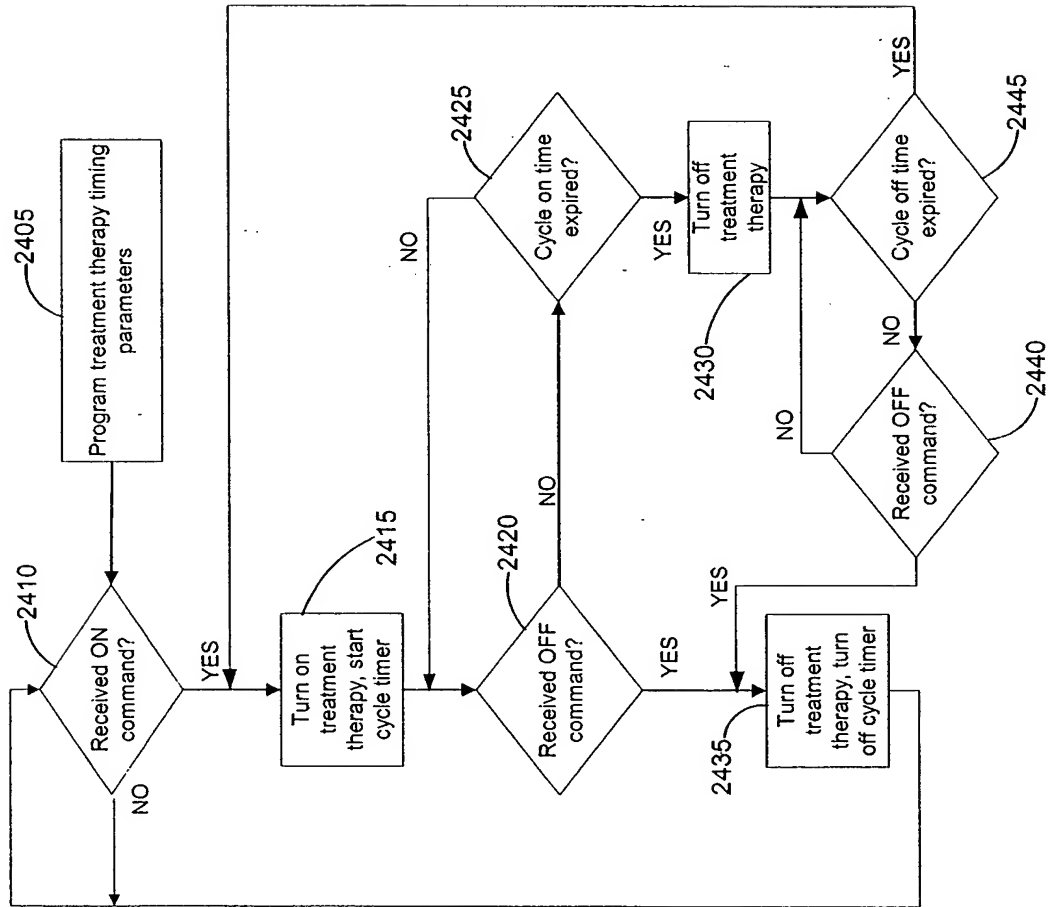


FIG. 23

FIG. 24





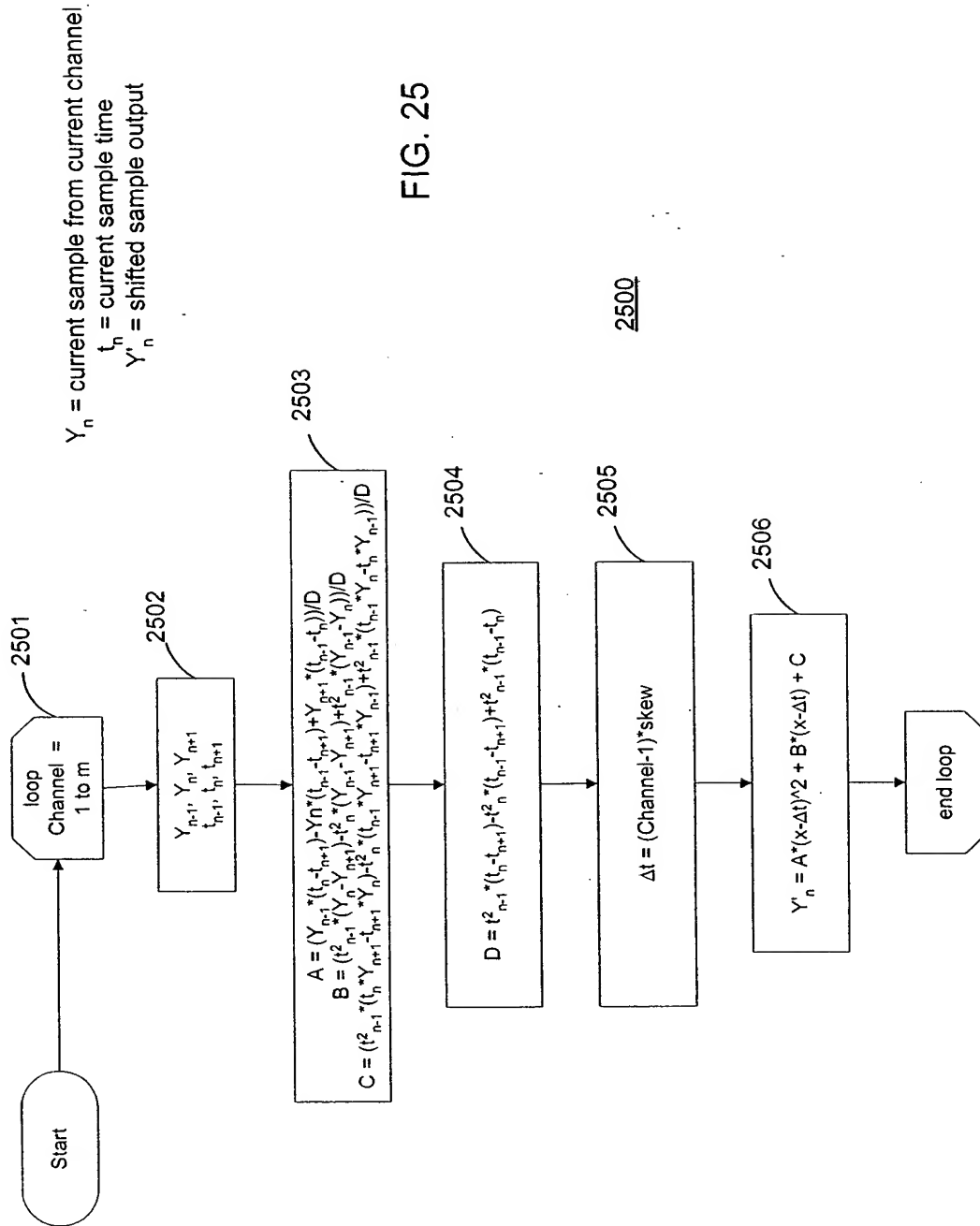
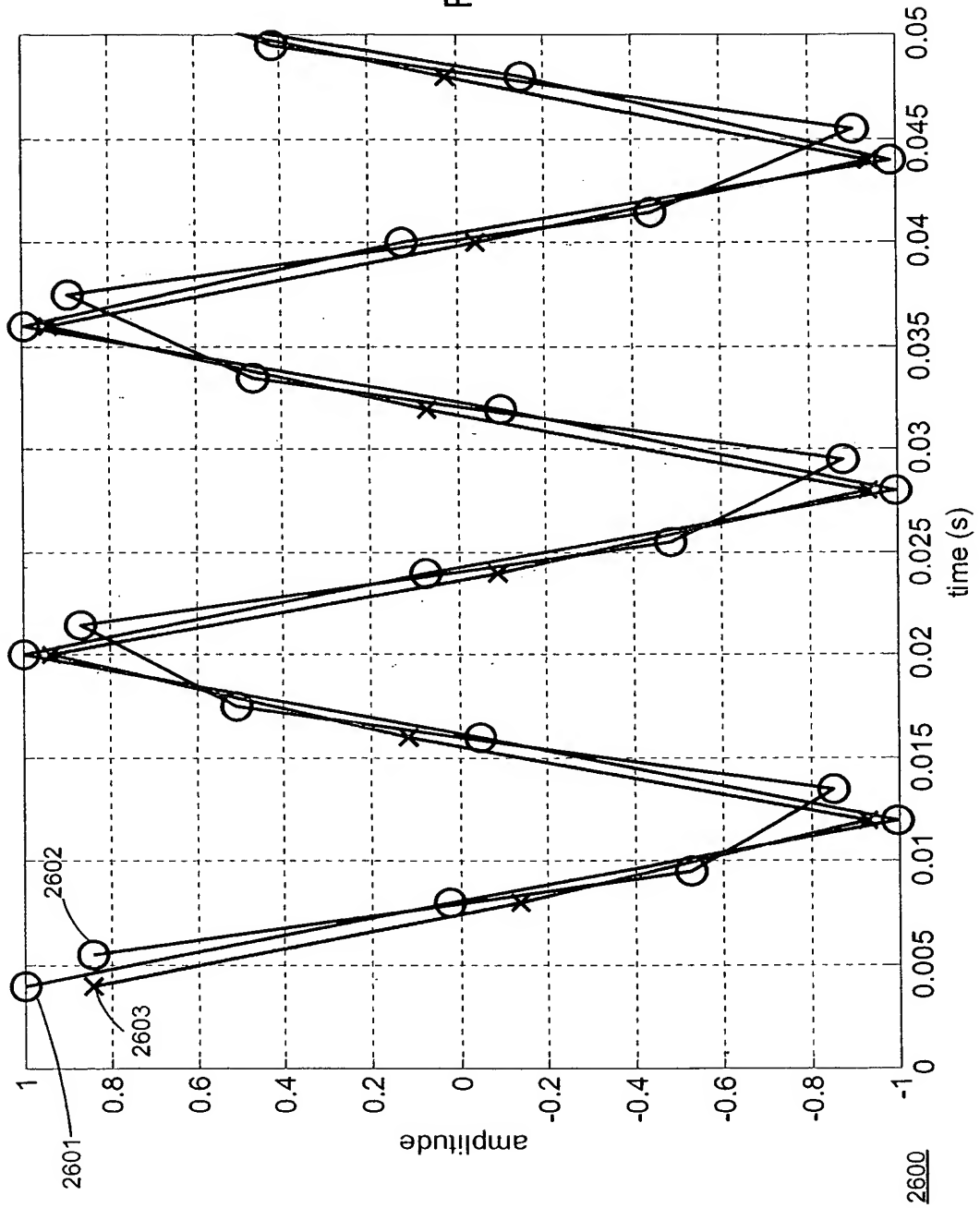


FIG. 26



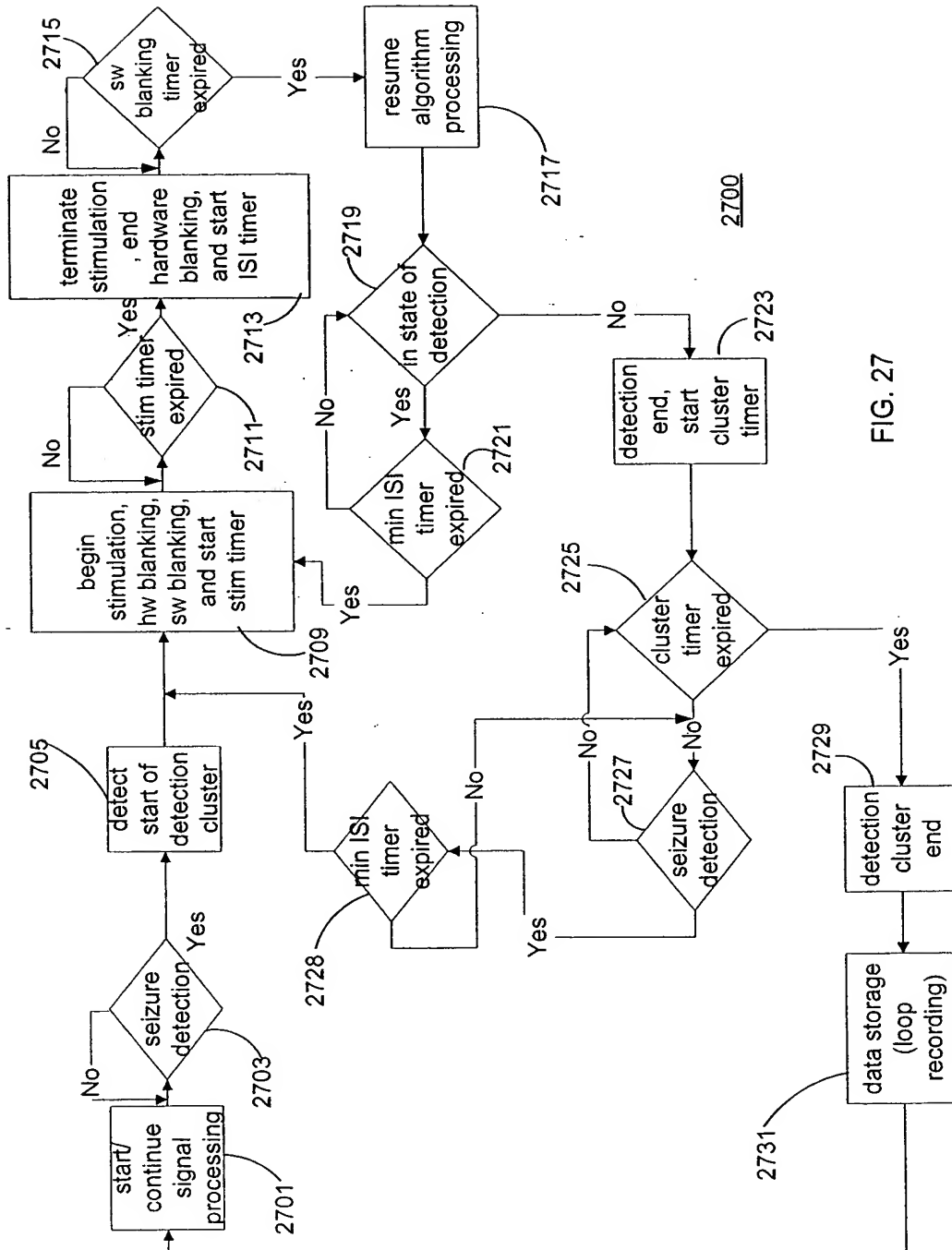


FIG. 27

FIG. 28

Example 1:  $z_{\min} = 0$ ,  $z_{\max} = 8$ , Empirical Data: {}

Graph of  $P(x; 0, 8, \{\})$ :

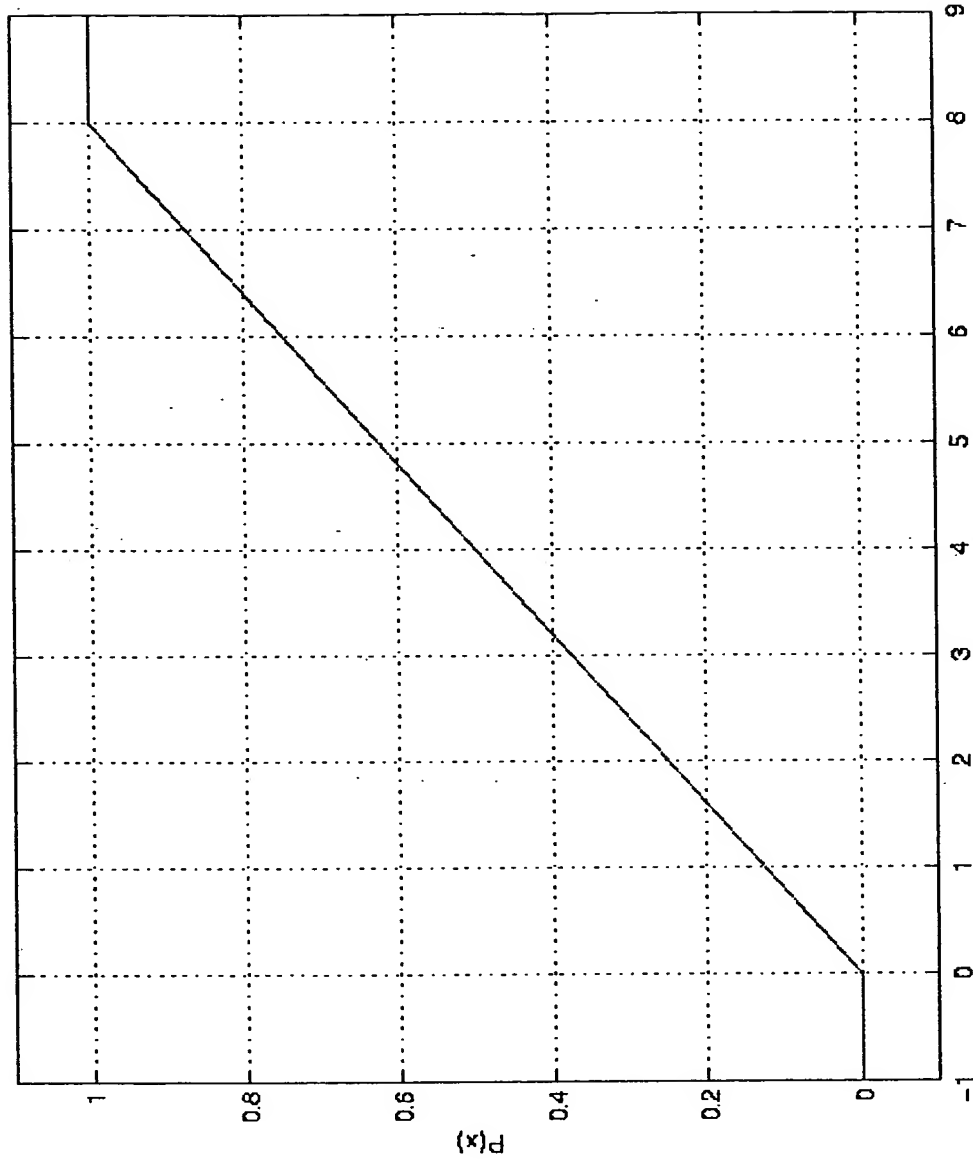
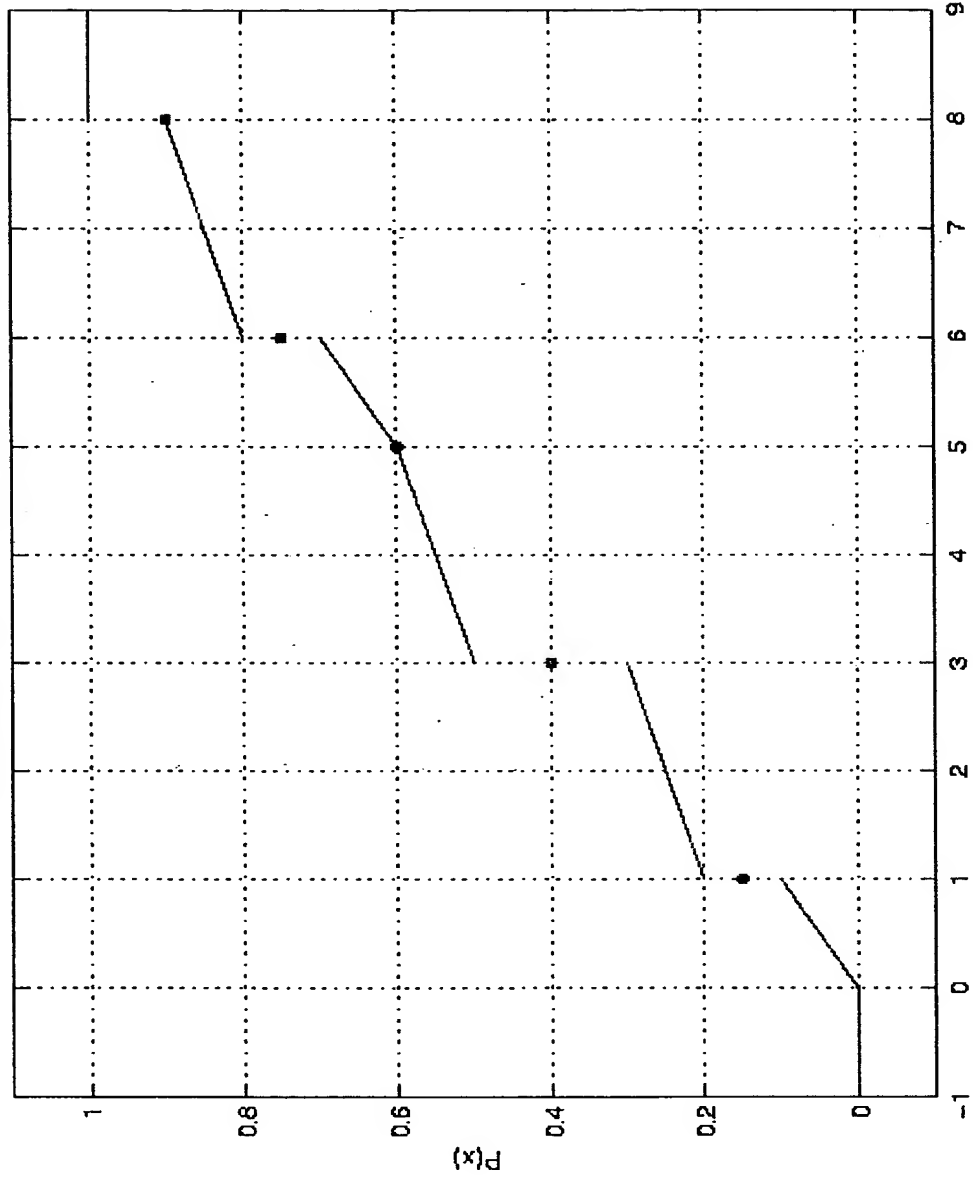


FIG. 29

Graph of  $P(x; 0, 8, \{1, 1, 3, 3, 3, 5, 6, 6, 8\})$ :

Example 2:  $z_{\min} = 0, z_{\max} = 8$ , Empirical Data:  $\{1, 1, 3, 3, 3, 5, 6, 6, 8\}$



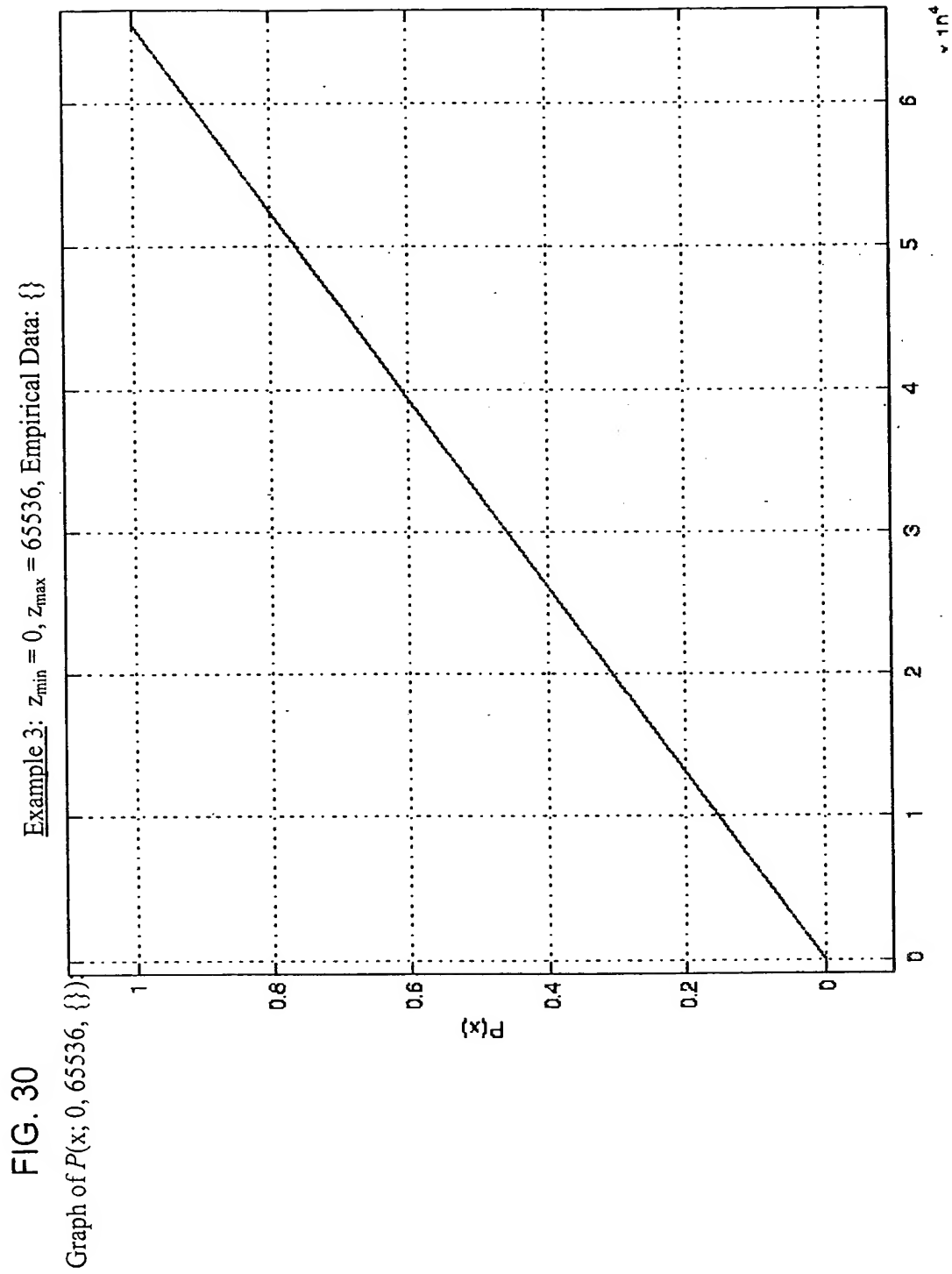
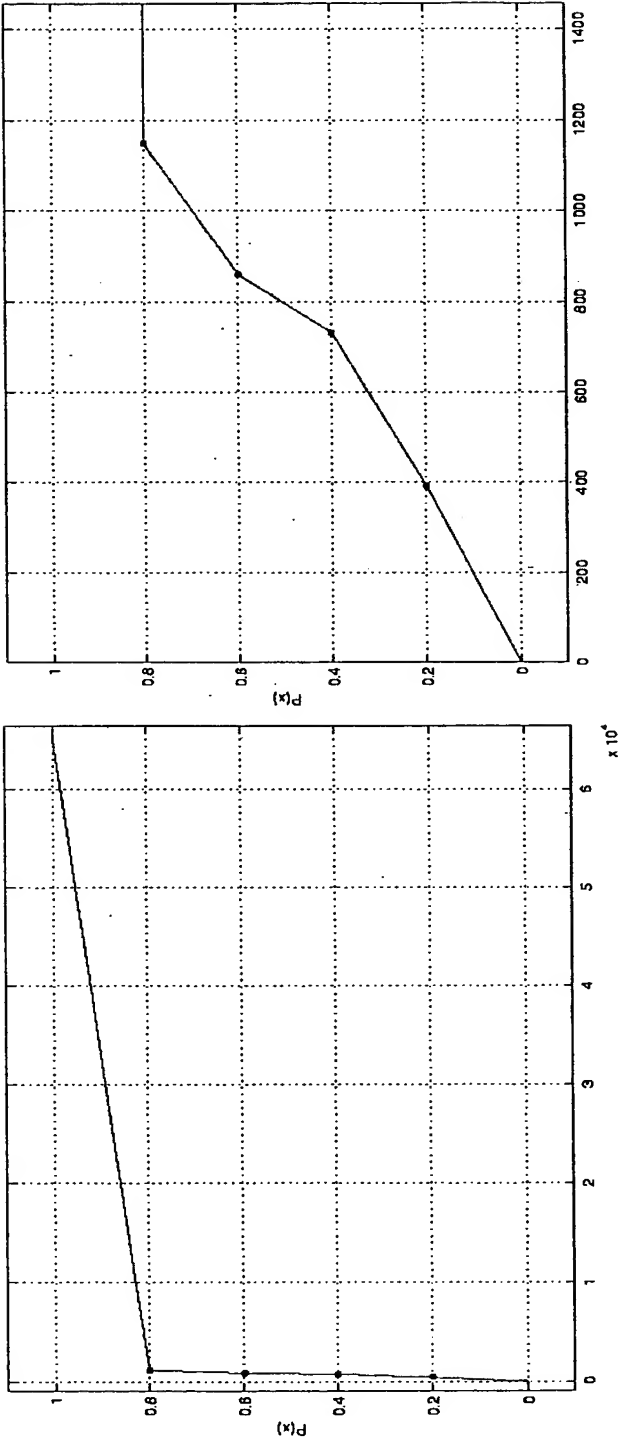


FIG. 31

Graph of  $P(x; 0, 65536, \{\text{round}([35.2, 41.3, 18.9, 55.1] * (250/12)))\} = P(x; 0, 65536, \{394, 733, 860, 1148\})$



Example 4:  $z_{\min} = 0, z_{\max} = 65536$ , Empirical Data: {923, 1058}

FIG. 32

Graph of  $P(x; 0, 6550, \{\})$

Example 5:  $z_{\min} = 0, z_{\max} = 6550$ , Empirical Data:  $\{\}$

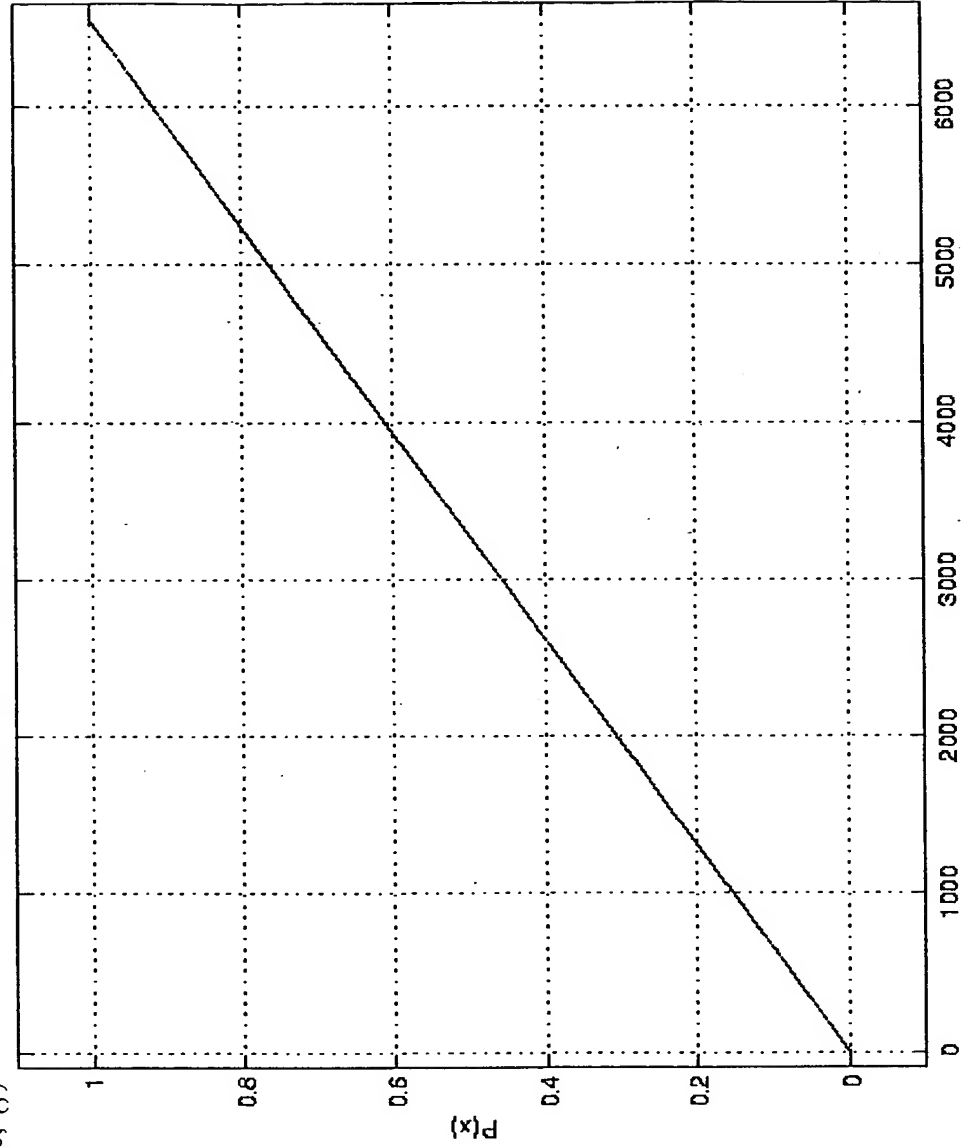
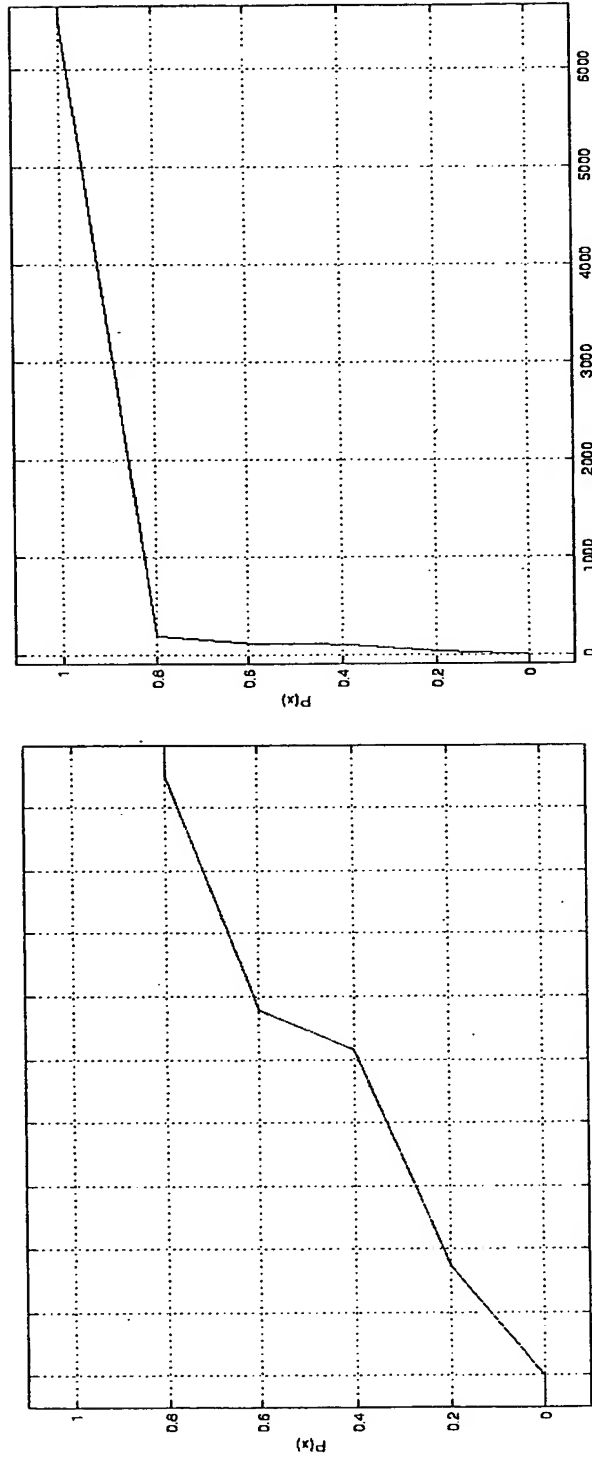




FIG. 33

Graph of  $P(x; 0, 6550, \{34.7, 103.1, 115.6, 189.9\})$



Example 6:  $Z_{\min} = 0, Z_{\max} = 6550$ , Empirical Data:  $\{34.7, 103.1, 115.6, 189.9\}$